

FIG. 1

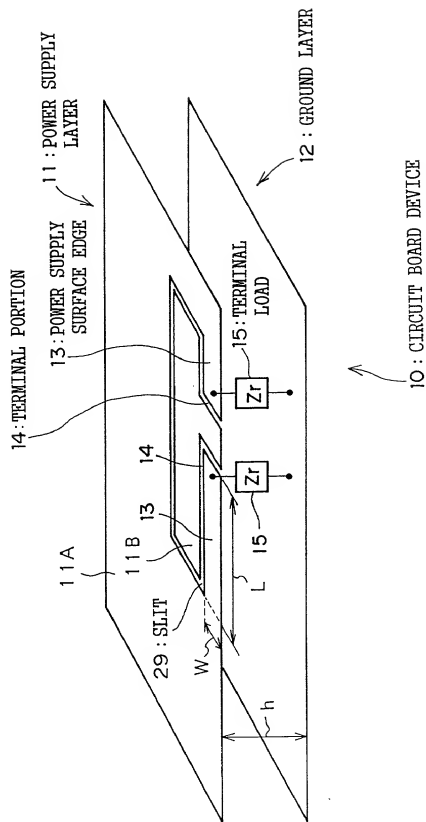
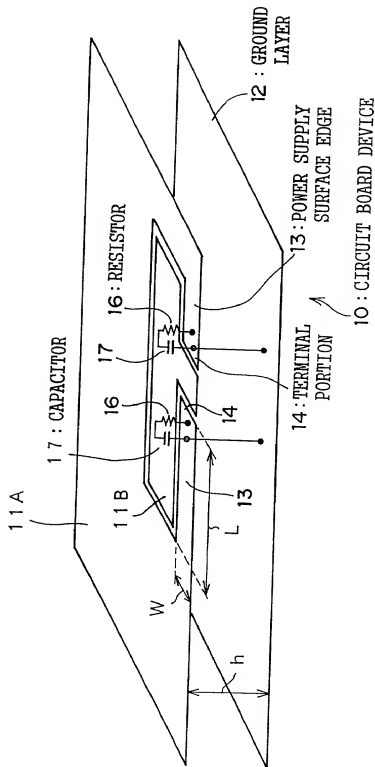


FIG. 2



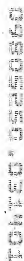
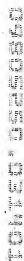
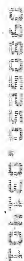


FIG. 4 A

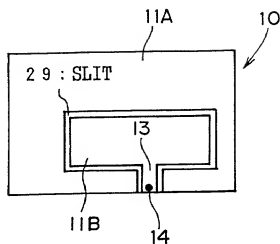


FIG. 4 B

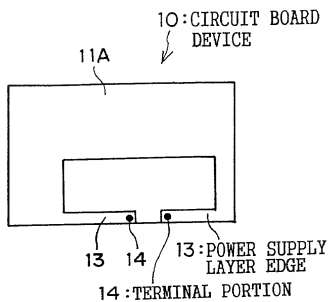


FIG. 4 C

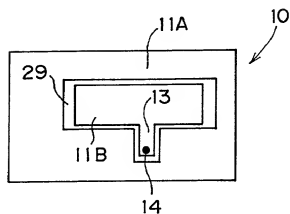


FIG. 4 D

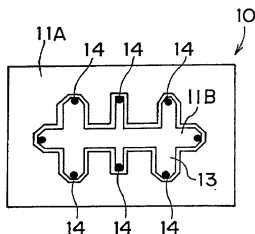


FIG. 4 E

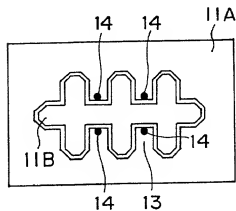
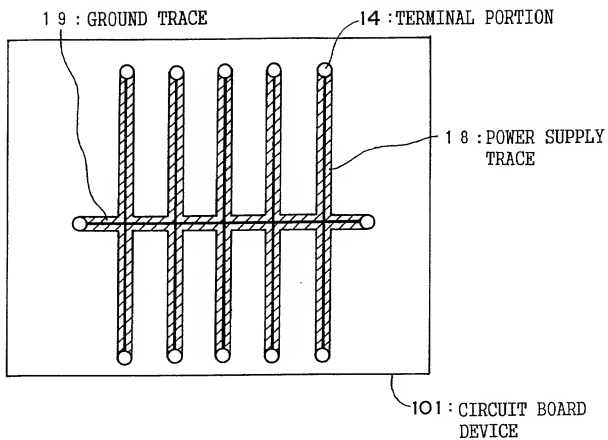
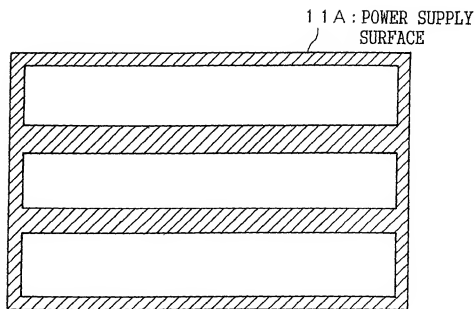


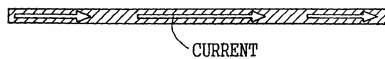
FIG. 5



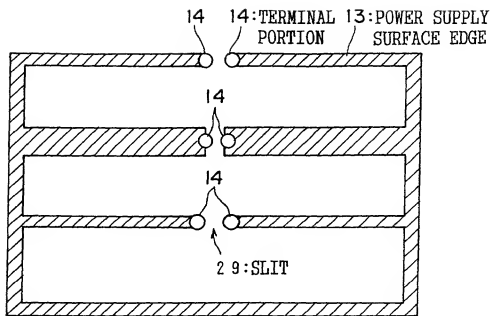
F I G. 6 A



F I G. 6 B



F I G . 7 A



F I G . 7 B

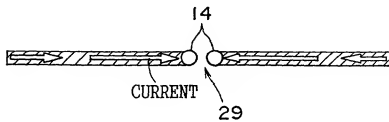


FIG. 8

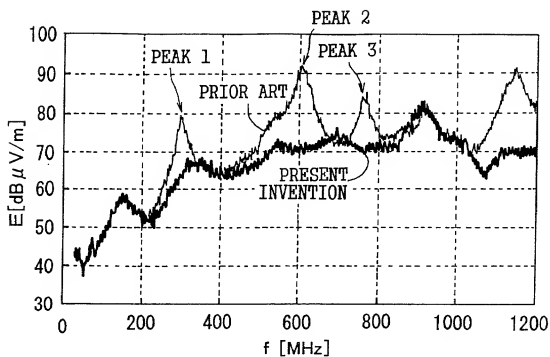


FIG. 9

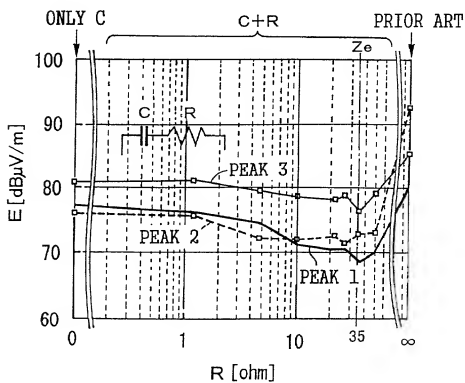




FIG. 10

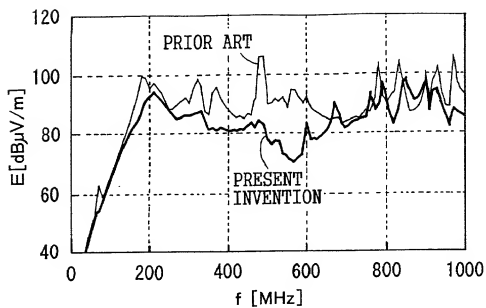


FIG. 11

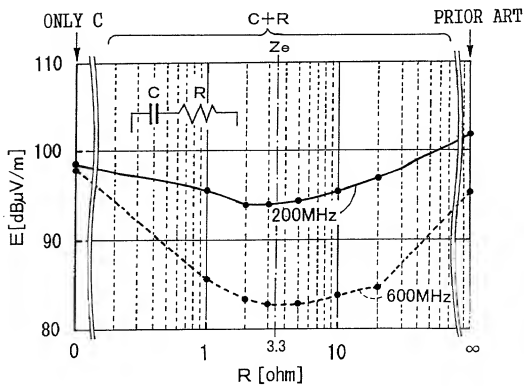


FIG. 12

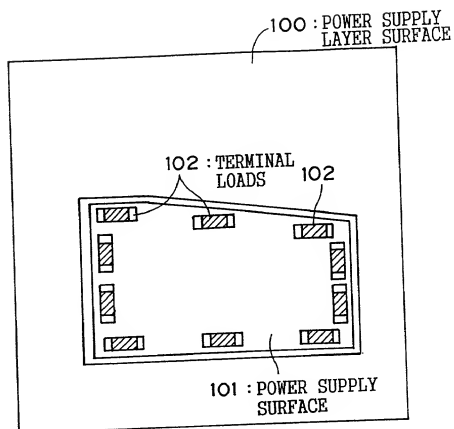


FIG. 13

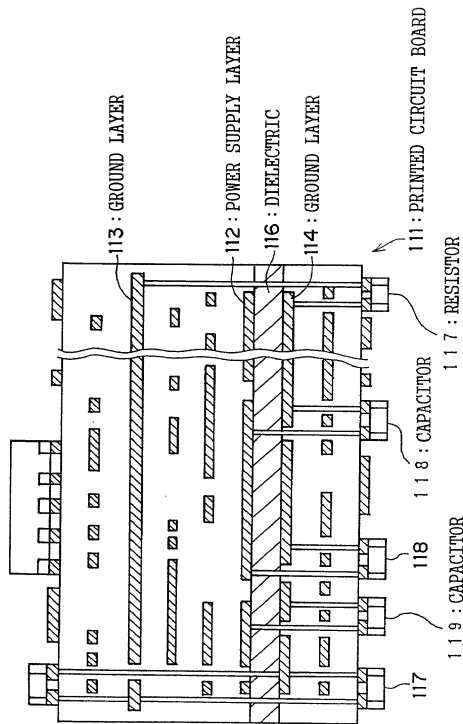


FIG. 14 A

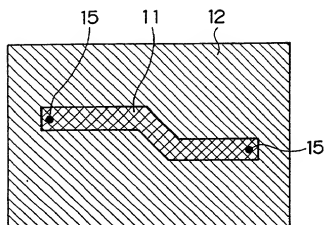


FIG. 14 B

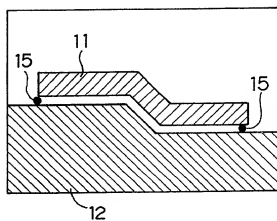


FIG. 14 C

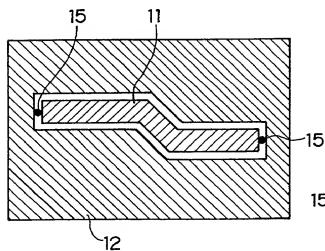
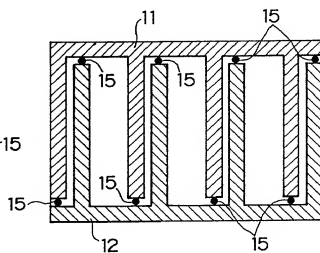
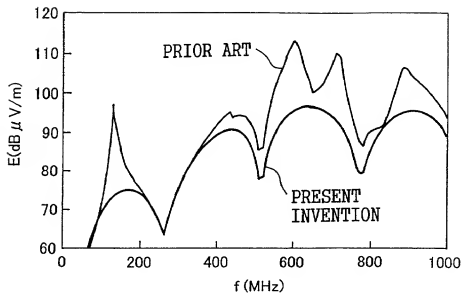


FIG. 14 D



F I G. 1 5 A



F I G. 1 5 B

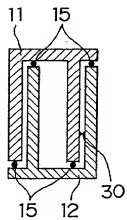


FIG. 16

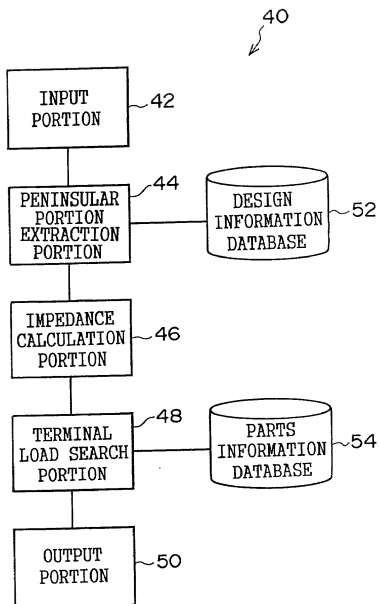


FIG. 17

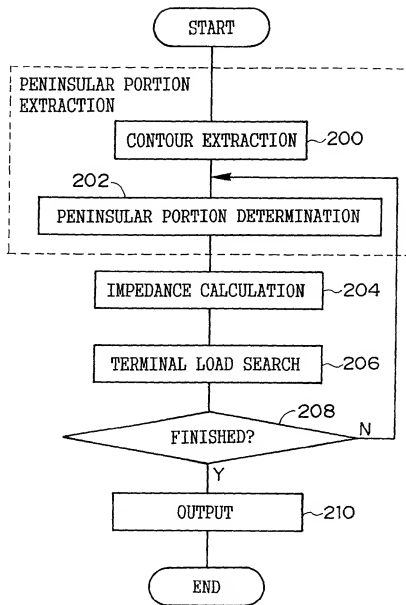


FIG. 18

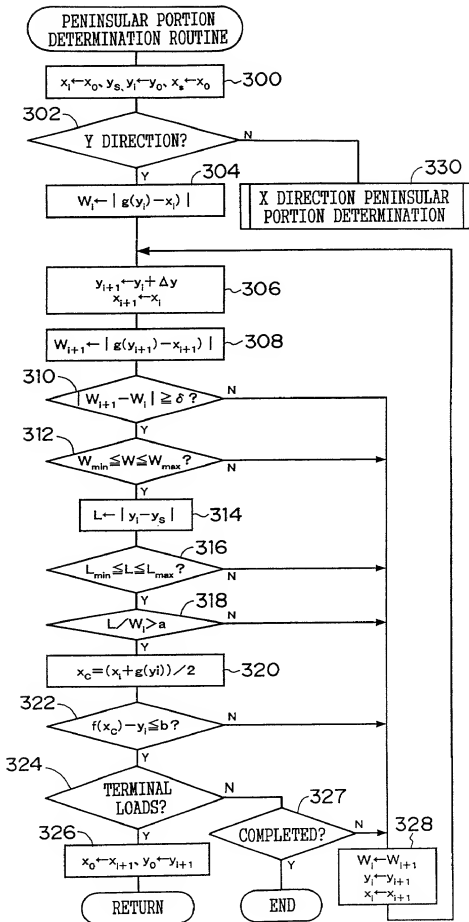




FIG. 19

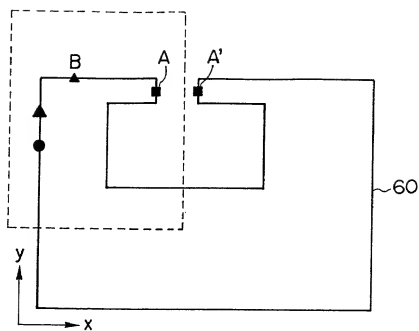


FIG. 20

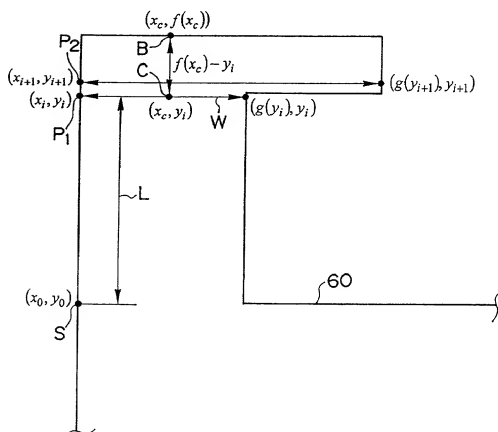


FIG. 21A

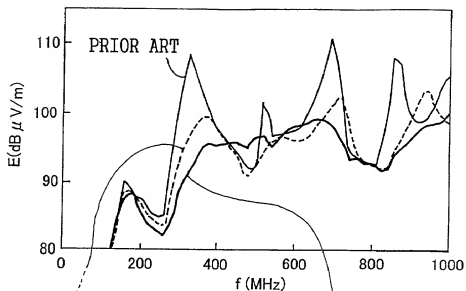
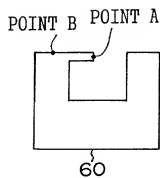


FIG. 21B



PRESENT INVENTION

(TERMINAL LOAD AT POINT A)

PRESENT INVENTION

(TERMINAL LOADS AT POINTS A AND B)

FIG. 22

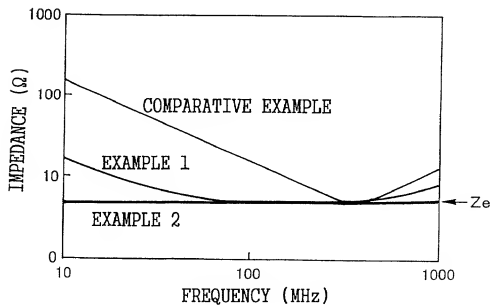


FIG. 23

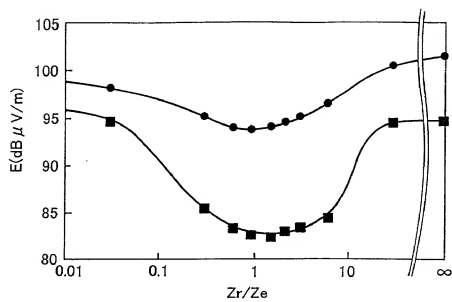


FIG. 24

